# Dirty Paper Coding using Sign-bit Shaping and LDPC Codes

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Abstract—Dirty paper coding (DPC) refers to methods for pre-subtraction of known interference at the transmitter of a multiuser communication system. There are numerous applications for DPC, including coding for broadcast channels. Recently, lattice-based coding techniques have provided several designs for DPC. In lattice-based DPC, there are two codes - a convolutional code that defines a lattice used for shaping and an error correction code used for channel coding. Several specific designs have been reported in the recent literature using convolutional and graph-based codes for capacity-approaching shaping and coding gains. In most of the reported designs, either the encoder works on a joint trellis of shaping and channel codes or the decoder requires iterations between the shaping and channel decoders. This results in high complexity of implementation. In this work, we present a lattice-based DPC scheme that provides good shaping and coding gains with moderate complexity at both the encoder and the decoder. We use a convolutional code for sign-bit shaping, and a low-density parity check (LDPC) code for channel coding. The crucial idea is the introduction of a onecodeword delay and careful parsing of the bits at the transmitter, which enable an LDPC decoder to be run first at the receiver. This provides gains without the need for iterations between the shaping and channel decoders. Simulation results confirm that at high rates the proposed DPC method performs close to capacity with moderate complexity. As an application of the proposed DPC method, we show a design for superposition coding that provides rates better than time-sharing over a Gaussian broadcast channel.

# I. INTRODUCTION

Situations where interference is known non-causally at the transmitter but not at the receiver model several useful multiuser communication scenarios. In [1], Costa introduced and studied coding for such situations and called it "writing on dirty paper". Dirty paper coding (DPC) is now recognized as a powerful notion central to approaching capacity on multiuser channels.

Lattice-based ideas for DPC were suggested and shown to be capacity-approaching in [2], [3]. Recently, many designs of lattice-based DPC schemes have been proposed in [4]–[8]. Lattice-based schemes typically use cosets of a convolutional code for lattice-quantizing or shaping to minimize the energy of the difference of the coded symbols and the interfering symbols. A part of the message bits is used to choose the specific coset used in the minimization. In addition to the shaping convolutional code, an error correction code needs to be used to obtain coding gain and approach capacity.

The main source of complexity in lattice-based DPC designs is combining shaping and coding encoders/decoders at the transmitter/receiver. Simple concatenation schemes are not applicable because of the following reasons - outer shaping followed by inner coding results in unshaped parity symbols that increase transmitted energy, while outer coding followed by inner shaping results in a poor inner code that needs to be iteratively decoded at the receiver with the outer code.

In [6], encoding is done on a combined trellis of the source code (Turbo TCQ) and a channel code (Turbo TCM). At the receiver, decoding is done for Turbo TCM followed by syndrome computation to recover message bits. The transmitter is complex in [6] because of the use of the joint trellis. The DPC method proposed in [7] is similar to that of [6]. In [5], multilevel coding is used, and there are different codes for different bits of the symbols. At the receiver, iterations have to be performed between decoders for some of the channel codes and the shaping decoder. In [8] and [9], shaping follows channel coding and the receiver performs iterations between the shaping and channel decoders.

In this work, we propose a lattice-based method that uses a novel combination of a convolutional code for sign-bit shaping and a low density parity check (LDPC) code for channel coding. As shown in specific designs and simulations, the method provides good shaping and coding gains at moderate complexity. The main idea for reducing complexity at the receiver is the introduction of a one-codeword delay at the transmitter, and the shaping of symbols from current message bits combined with parity bits from the previous codeword. This enables the LDPC decoder to be run first at the receiver (with a one-codeword delay) without any need for iterations with a shaping decoder. As an application, we use the proposed DPC method to design codes for superposition coding in two-user Gaussian broadcast channels. By simulations, we show that rate points outside the time-sharing region are achieved.

The rest of the paper is organized as follows. After a brief review of the lattice-based DPC coding method in Section II, we present the proposed DPC method in Section III. This is followed by description and simulation of specific designs of DPC codes in Section IV. In Section V, design of a superposition scheme using the proposed DPC method is described and simulation results are presented. Concluding remarks are made in Section VI.

### II. LATTICE DIRTY PAPER CODES

In a Gaussian dirty paper channel, the received symbol vector  $\mathbf{Y} = [Y_1 \ Y_2 \cdots Y_n]$  is modeled as

$$Y = X + S + N,$$

where  $\mathbf{X} = [X_1 \ X_2 \cdots X_n]$  denotes the transmitted vector,  $\mathbf{S} = [S_1 \ S_2 \cdots S_n]$  denotes the interfering vector assumed to be known non-causally at the transmitter and N denotes the additive Gaussian noise vector. The transmit power is assumed to be upper-bounded by  $\frac{1}{n}E[|\mathbf{X}|^2] \leq P_X$  per symbol, and the interference power is denoted  $\frac{1}{n}E[|\mathbf{S}|^2] = P_S$  per symbol. The noise variance per symbol is denoted  $P_N$ . In [1], Costa shows that the capacity of the dirty paper channel is  $\frac{1}{2}\log\left(1+\frac{P_X}{P_N}\right)$  i.e. known interference can be canceled perfectly at the transmitter.

The interfering vector S is used as an input in the encoding process and plays an important role to determine a suitable transmit vector X. A coding strategy for choosing X needs to overcome the imminent addition of S and protect the transmitted information from the addition of the noise N. Such coding strategies are called dirty paper coding (DPC) methods.

In [3], a dirty paper coding (DPC) scheme based on lattice strategies was proposed and shown to achieve the capacity of the dirty paper channel. We follow [4] for a brief review of the transmitter and receiver structure in the lattice DPC method [3]. Let  $\Lambda$  denote an n-dimensional lattice with fundamental Voronoi region  $\nu$  having averaged second moment  $P(\Lambda) = P_X$  and normalized second moment  $G(\Lambda)$ . Also let  $U \sim \text{Unif}(\nu)$  i.e. U is a random variable (dither) uniformly distributed over  $\nu$ . The lattice transmission approach of [3] [4] is as follows.

• Transmitter: The input alphabet X is restricted to  $\nu$ . For any  $v \in \nu$ , the encoder sends

$$X = [\mathbf{v} - \alpha \mathbf{S} - \mathbf{U}] \bmod \Lambda, \tag{1}$$

where  $\alpha = \frac{P_X}{P_X + P_N}$  is a MMSE scaling factor [4]. • *Receiver*: The receiver computes

$$\mathbf{Y}' = [\alpha \mathbf{Y} + \mathbf{U}] \bmod \Lambda \tag{2}$$

The channel from v to Y' defined by (1) and (2) is equivalent in distribution to

$$Y' = [\nu + N'] \bmod \Lambda, \tag{3}$$

where

$$N' = [(1 - \alpha) U + \alpha N] \mod \Lambda. \tag{4}$$

Lower bounds on achievable rates for the above equivalent channel is shown in [4] to be equal to

$$I(V;Y') \ge \frac{1}{2}\log_2(1 + SNR) - \frac{1}{2}\log_2(2\pi eG(\Lambda)).$$
 (5)

For optimal shaping,  $G\left(\Lambda\right) \to \frac{1}{2\pi e}$  and we approach capacity of the dirty paper channel. Note that the dither is assumed to be known at the transmitter and receiver (say, through the use of a common seed in a random number generator).

# III. PROPOSED SCHEME

The proposed scheme uses a convolutional code for signbit shaping [10] and low density parity check (LDPC) codes for channel coding. We assume a M-PAM signal constellation with a carefully chosen bit-to-symbol mapping that is compatible with sign-bit shaping and bit-interleaved coded modulation (BICM) [11]. For M=16, the constellation and mapping are shown in Fig. 1. The mapping in Fig. 1 is suited for signbit shaping, since a flip of the most significant bit results in a significant change in symbol value for all possible 4-bit inputs. Also, the mapping is mostly Gray except for a few symbol transitions. Gray mapping is known to be the most effective mapping for BICM with LDPC codes. This heuristic choice of mapping enables the possibility of good shaping and coding gains to be obtained simultaneously. As expected, larger values of M will result in larger shaping gains in our design, and we stick to the 16-PAM shown in Fig. 1 for illustration and simulation.

# A. Encoder Structure

The encoder structure for the proposed scheme is as shown in Fig. 2. We describe the operations in the encoder at time step T or in the T-th block. A k-bit message  $\mathbf{m} = [m_1 \ m_2 \cdots m_k]$  is encoded into a s-symbol vector  $\mathbf{u} = \begin{bmatrix} u_1 & u_2 \cdots u_s \end{bmatrix}$  from the M-PAM constellation  $A = \{-(M-1)/2, \cdots, -1/2, 1/2, \cdots, (M-1)/2\},$  where  $s = \frac{n}{\log_2 M}$  is assumed to be an integer. Let  $l = \log_2 M$  and let  $f_M: \{0,1\}^l \to A$  denote the bit-to-symbol mapping. The bits that map to the *i*-th symbol are denoted  $z_i a_{2i} a_{3i} \cdots a_{li}$ ; the sign-bit vector is denoted  $\mathbf{z} = [z_1 \ z_2 \cdots z_s]$ , and we define vectors  $\mathbf{a}_j = [a_{j_1} \ a_{j_2} \cdots a_{j_s}]$  for  $2 \leq j \leq l$ . Finally, we have  $\mathbf{v} = f_M(\mathbf{z}\mathbf{a_2}\cdots\mathbf{a_l})$ , where  $f_M$  operates component-wise on a vector input.

Let us assume that the vectors  $\mathbf{a}_i$ ,  $2 \le j \le l$  are available at the encoder. The sign-bit shaping convolutional code is used to determine the sign-bit vector z as follows. A part of the message  $\mathbf{m}' = [m_1 \ m_2 \cdots m_{k'}]$  with k' < k bits is mapped to a coset leader of the convolutional code using an inverse syndrome former [10]. Note that we need the rate of the convolutional code to be  $1-k^{\prime}/s$ . Let the coset chosen by  $\mathbf{m}'$  be denoted  $C(\mathbf{m}')$ . The sign-bit vector  $\mathbf{z}$  is chosen from  $C(\mathbf{m}')$  so as to minimize the squared sum (energy) of the vector  $(\mathbf{v} - \alpha \mathbf{S}) \mod M$ , where  $\alpha = \frac{P_X}{P_X + P_N}$  is the MMSE factor and S is the interference vector. That is,

$$\mathbf{z} = \arg\min_{\mathbf{u} \in C(\mathbf{m}')} |(f_M(\mathbf{u}\mathbf{a}_2 \cdots \mathbf{a}_l) - \alpha \mathbf{S}) \mod M|^2. \quad (6)$$

The minimization in (6) is implemented using the Viterbi algorithm [10].

The  $a_j$ ,  $2 \le j \le l$  are determined as follows. An (n, k-k'+s) LDPC code is used at the encoder with a systematic encoder  $E: \{0,1\}^{k-k'+s} \to \{0,1\}^n$ . Let  $\mathbf{m}'' = [\mathbf{z} \ m_{k'+1} \cdots m_k]$  be input to the systematic LDPC encoder to obtain the codeword  $E(\mathbf{m''}) = [\mathbf{m''} \ \mathbf{p}_T],$  where  $\mathbf{p}_T$  is the parity-bit vector for the T-th block. The parity-bit vector is delayed by one time step. For the T-th block, the n-s=s(l-1) bits in  $[m_{k'+1}\cdots m_k]$ 

Fig. 1. 16-PAM constellation.

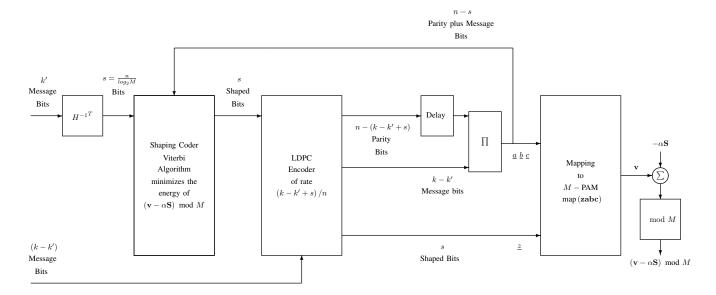


Fig. 2. Encoder structure.

and  $\mathbf{p}_{T-1}$  are rearranged by a permutation  $\Pi$  to form the vectors  $\mathbf{a}_j$ ,  $2 \leq j \leq l$ . This permutation is necessary in an implementation of BICM [11].

Note that both the shaping and coding objectives have been met at the encoder. The transmitted symbols  $\mathbf{v} - \alpha \mathbf{S} \mod M$  have minimal energy in the lattice defined by signbit shaping using the convolutional code. Selected bits in successive blocks of symbols form codewords of the LDPC code. In summary, the encoder structure achieves DPC shaping and LDPC coding with bit-interleaved modulation.

# B. Decoder Structure

The decoder for the proposed scheme is as shown in Fig.3. The demapper computes log likelihood ratios (LLRs) for the bits from the received symbols in  $\widehat{\mathbf{Y}} = \alpha \mathbf{Y} + \mathbf{U}$ . The LLRs of the (k-k') message bits after a delay of one time step, and the LLRs of the n-(k-k'+s) parity bits are de-interleaved. The  $s=\frac{n}{\log_2 M}$  LLRs of the sign bits after a delay on one time step, and the n-s output LLRs of the de-interleaver are given as the input to the LDPC decoder. The LDPC decoder outputs k-k' message bits and s bits of the sign bit vector of the previous block. Now, the s-bit sign vector is passed through the syndrome former to recover the remaining k' message bits.

The demapper function at the receiver has to calculate LLRs taking into account the modulo M operation at the encoder

[4]. Therefore, the received constellation  $A_R$  is a replicated version of the M-PAM constellation A used at the transmitter (assuming that scaling factors have been corrected at the receiver). That is,

$$A_R = \{A - rM, \cdots, A - M, A, A + M, \cdots, A + rM\}.$$

The number of replications r is chosen so that the average power of  $A_R$  is approximately equal to the total average power  $P_X + P_S$ , and the bit mapping of the symbol a + jM ( $a \in A, 1 \le j \le r$ ) is the same as that for a. The LLR for the i-th bit in the j-th symbol  $\widehat{Y}_j$  is computed according to the constellation  $A_R$  using the following formula:

$$L_i = \frac{\sum\limits_{a \in A_R: \text{bit } i=0} \exp\left(-\frac{1}{2}\frac{\left(\widehat{Y}_j - a\right)^2}{\alpha P_N}\right)}{\sum\limits_{a \in A_R: \text{bit } i=1} \exp\left(-\frac{1}{2}\frac{\left(\widehat{Y}_j - a\right)^2}{\alpha P_N}\right)}.$$

Since the constellation mapping is nearly Gray, iterations with the demapper do not provide significant improvements in coding gain, particularly for large M.

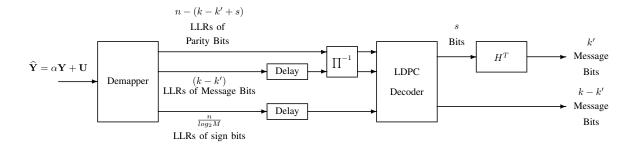


Fig. 3. Decoder structure.

## IV. SIMULATION RESULTS

For simulations, we have taken n=40000, k=30000, k'=5000 with M=16; this results in s=10000. The constellation mapping is as given in Fig. 1. We have chosen a rate-1/2 memory 8 (256 state) non-systematic convolutional code with generator polynomials  $\left(D^8+D^5+D^4+D^2+D+1,\ D^8+D^7+D^4+D^2+1\right)$  as the sign-bit shaping code. A non-systematic convolutional code is used to avoid error propagation problems.

A randomly constructed irregular LDPC code (40000, 35000) of rate 7/8 with variable node degree distribution:  $0.1256x + 0.7140x^2 + 0.1604x^9$  and check node degree distribution  $x^{31}$  is used as the channel code. The overall rate of transmission is seen to be  $\frac{30000}{40000} \times 4 = 3$  bits per channel use. Fig. 4 shows BER plots over an AWGN channel and a DPC channel with interference known at the transmitter. The

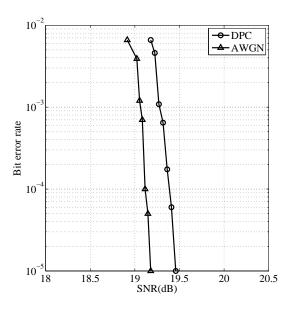


Fig. 4. BER plot for DPC and AWGN.

interfering vector was generated at random for different power levels. The plot with interference did not change appreciably for all power levels of interference, and we have provided one plot for illustration. We see that a BER of  $10^{-5}$  is achieved at a SNR of 19.45dB with interference, and at a SNR of 19.33 dB without interference. We have simulated 1000 blocks of length 40000 to obtain sufficient statistics for a BER of  $10^{-5}$ .

The AWGN capacity at an SNR of  $10\log_{10}(2^6-1)=17.99$  dB for a rate of 3 bits per channel use. This shows that we are 1.46 dB away from ideal dirty paper channel capacity. The granular gain  $G(\Lambda)=2^{C^*}/6S_x$  is computed from the simulations to be 1.282dB [4], where  $C^*=3.5$  is the rate before channel coding, and  $S_x$  is the transmit power (obtained through simulations). From this, the shaping loss is calculated as follows:

$$10\log_{10} \frac{2\pi eG\left(\Lambda\right)2^{2C^*} - 1}{2^{2C^*} - 1} = 0.2548 \text{ dB}.\tag{7}$$

So, of the total gap of 1.46 dB, we have a shaping gap of 0.2548dB, and a coding gap of 1.2052dB to capacity.

### V. APPLICATION TO GAUSSIAN BROADCAST CHANNEL

We use the proposed scheme for superposition coding in a two-user Gaussian broadcast channel  $Y_1 = X + N_1$  and  $Y_2 = X + N_2$  with  $P_{N_1} > P_{N_2}$ . We let  $P_{X_1} = (1 - \beta) P$  and  $P_{X_2} = \beta P$ , where P is the total transmit power. Here, User 2 is coded using DPC considering User 1 as interference. User 1 is shaped using sign-bit shaping and coded using an LDPC code over M-PAM. Fig. 5 shows a block diagram of the transmitter and receivers. The encoder structure for User 1 is as in Fig. 2 with the interference vector  $\mathbf{S} = \mathbf{0}$ . Hence, for User 1, the shaping coder minimizes the energy of  $\mathbf{v}$ . The demapper at Receiver 1 calculates LLR for the i-th bit in the j-th receiver symbol  $Y_{1j}$  using the following formula.

$$L_{i} = \frac{\sum_{a \in A: \text{bit } i=0} \left\{ p(a) \exp\left\{ -\frac{1}{2} \frac{(Y_{1j} - a)^{2}}{\beta P + P_{N_{1}}} \right\} \right\}}{\sum_{a \in A: \text{bit } i=1} \left\{ p(a) \exp\left\{ -\frac{1}{2} \frac{(Y_{1j} - a)^{2}}{\beta P + P_{N_{1}}} \right\} \right\}},$$

where p(a) for  $a \in A$  represents the *a priori* probability of the M-PAM symbol a. At the receiver, we approximate  $p_i$  using a Gaussian distribution with variance  $P_S$  assuming that the distribution of M-PAM symbols is approximately Gaussian.

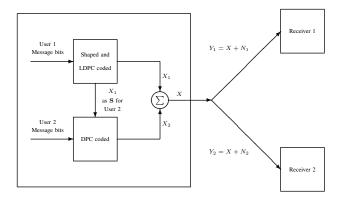


Fig. 5. Block diagram for a two-user Gaussian broadcast channel.

We simulated a two user degraded broadcast channel with  $P_{N_1}=0.9$  and  $P_{N_2}=0.09$  using the proposed scheme with parameters from Section IV. The total transmit power, power for User 1 and power for User 2 required for a bit error rate of  $10^{-5}$  (at both receivers) are estimated from the simulation and denoted P,  $P_{x_1}$  and  $P_{X_2}$ , respectively. The SNR for Receiver 1 is computed as  $10\log_{10}\left(\frac{P_{X_1}}{P_{X_2}+P_{N_1}}\right)=19.1791$  dB. Since DPC is done for User 2, the effective SNR at Receiver 2 is computed as  $10\log_{10}\left(\frac{P_{X_2}}{P_{N_2}}\right)=19.4574$  dB. Comparison with the SNR needed for a single user capacity of 3 bits per channel use (which is 17.99 dB) shows that the total loss for both the users is about 2.4642dB. Fig. 6 shows the (3, 3) rate pair in the capacity region of the two-user Gaussian broadcast channel with total transmit power P and noise power  $P_{N_1}$ ,  $P_{N_2}$ , which is defined by  $R_1 \leq \frac{1}{2}\log\left(1+\frac{(1-\beta)P}{\beta P+P_{N_1}}\right)$ ,  $R_2 \leq \frac{1}{2}\log\left(1+\frac{\beta P}{P_{N_2}}\right)$  for  $0 \leq \beta \leq 1$ . We see that the (3,3) rate point is clearly outside the time-sharing region.

# VI. CONCLUSIONS

In this work, we have proposed a method for designing lattice-based schemes for dirty paper coding using sign-bit shaping and LDPC codes. Simulation results show that the proposed design performs 1.46dB away from the dirty paper capacity for a block length of n=40000 at the rate of 3 bits/channel use. This performance is comparable to other results in the literature. However, as discussed in this article, a novel method for combining shaping and coding results in good gains at lesser complexity in our design, when compared to other lattice-based strategies. As an application, we have designed a superposition coding scheme for Gaussian broadcast channels that is shown to perform better than time-sharing through simulations.

Out of the 1.46 dB gap to capacity, about 1.2 dB is gap attributed to a sub-optimal choice of the LDPC code. Optimizing the LDPC code will require use of genetic algorithms and asymmetric density evolution [12], which are topics for future work.

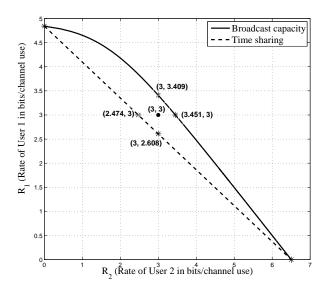


Fig. 6. Two-user Gaussian broadcast channel capacity region.

### REFERENCES

- [1] M. Costa, "Writing on dirty paper (corresp.)," *Information Theory, IEEE Transactions on*, vol. 29, no. 3, pp. 439–441, May 1983.
- [2] T. Philosof, U. Erez, and R. Zamir, "Combined shaping and precoding for interference cancellation at low snr," in *Information Theory*, 2003. Proceedings. IEEE International Symposium on, June-4 July 2003, pp. 68–
- [3] U. Erez, S. Shamai, and R. Zamir, "Capacity and lattice strategies for canceling known interference," *Information Theory, IEEE Transactions* on, vol. 51, no. 11, pp. 3820–3833, Nov. 2005.
- [4] U. Erez and S. Brink, "A close-to-capacity dirty paper coding scheme," Information Theory, IEEE Transactions on, vol. 51, no. 10, pp. 3417–3432, Oct. 2005.
- [5] S.-Y. Chung, "Multi-level dirty paper coding," Communications Letters, IEEE, vol. 12, no. 6, pp. 456–458, June 2008.
- [6] Y. Sun, M. Uppal, A. Liveris, S. Cheng, V. Stankovic, and Z. Xiong, "Nested turbo codes for the costa problem," *Communications, IEEE Transactions on*, vol. 56, no. 3, pp. 388–399, March 2008.
- [7] M. Uppal, V. Stankovic, and Z. Xiong, "Code design for mimo broadcast channels," *Communications, IEEE Transactions on*, vol. 57, no. 4, pp. 986–996, April 2009.
- [8] Y. Sun, Y. Yang, A. Liveris, V. Stankovic, and Z. Xiong, "Near-capacity dirty-paper code design: A source-channel coding approach," *Information Theory, IEEE Transactions on*, vol. 55, no. 7, pp. 3013–3031, July 2009.
- [9] Y. Sun, A. Liveris, V. Stankovic, and Z. Xiong, "Near-capacity dirty-paper code designs based on tcq and ira codes," in *Information Theory*, 2005. ISIT 2005. Proceedings. International Symposium on, Sept. 2005, pp. 184–188.
- [10] J. Forney, G.D., "Trellis shaping," Information Theory, IEEE Transactions on, vol. 38, no. 2, pp. 281–300, Mar 1992.
- [11] G. Caire, G. Taricco, and E. Biglieri, "Bit-interleaved coded modulation," *IEEE Trans. on Info. theory*, vol. 44, no. 3, pp. 927–946, May 1998.
- [12] C.-C. Wang, S. Kulkarni, and H. Poor, "Density evolution for asymmetric memoryless channels," *Information Theory, IEEE Transactions on*, vol. 51, no. 12, pp. 4216–4236, Dec. 2005.